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Terms	Documents
L7 and (battery with (independent\$3 or intervention) with (power or current or voltage))	0

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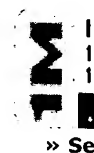
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<u>L8</u>	L7 and (battery with (independent\$3 or intervention) with (power or current or voltage))	0	<u>L8</u>
<u>L7</u>	L6 and ((latch or register) near6 (set\$4 or initiali\$6 or state))	58	<u>L7</u>
<u>L6</u>	L5 with (power or current or voltage) with (latch or register)	369	<u>L6</u>
<u>L5</u>	charg\$4 near4 (pump or circuit or device)	136159	<u>L5</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>			
<u>L4</u>	L3 and (battery with (independent\$3 or intervention) with (power or current or voltage))	36	<u>L4</u>
<u>L3</u>	L2 and ((latch or register) near6 (set\$4 or initiali\$6 or state))	603	<u>L3</u>
<u>L2</u>	L1 with (power or current or voltage) with (latch or register)	1096	<u>L2</u>
<u>L1</u>	charg\$4 near4 (pump or circuit or device)	149461	<u>L1</u>

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1 [Pen computing: a technology overview and a vision](#)

André Meyer

July 1995

ACM SIGCHI Bulletin, Volume 27 Issue 3

Full text available: pdf(5.14 MB)

[Additional Information: f](#)

This work gives an overview of a new technology that is attracting growing interest in public as we means of interaction between a user and a machine, picking up the familiar pen and paper interface and visions. Starting with a short historic ...

2 [Modeling the Power Consumption of Audio Signal Processing Computations Using Customi](#)

Roger Chamberlain, Eric Hemminger, Robert Morley, Jason White

March 2003

Proceedings of the 36th annual symposium on Simulation

Full text available: pdf(151.09 KB) [Publisher Site](#)

[Additional Information: f](#)

This paper explores the impact that numerical representation has on the power consumption of audio design goal. We investigate two aspects of this problem. First, we evaluate the validity of using signal consumption of multiply-accumulate operations for several ...

Keywords: audio signal processing, power consumption, numerical representation

3 [An architecture for voice dialog systems based on prolog-style theorem proving](#)

Ronnie W. Smith, Alan W. Biermann, D. Richard Hipp

September 1995

Computational Linguistics, Volume 21 Issue 3

Full text available: pdf(2.76 MB) [Publisher Site](#)

[Additional Information: f](#)

A pragmatic architecture for voice dialog machines aimed at the equipment repair problem has been include: (1) problem solving to achieve a target goal (2) the ability to carry out subdialogs to achieve verbal exchanges and to inhibit unnecessary ones(...

4 [Power minimization in IC design: principles and applications](#)

Massoud Pedram

January 1996

ACM Transactions on Design Automation of Electronic Systems (TO

Full text available: pdf(550.02 KB)

[Additional Information: f](#)

Low power has emerged as a principal theme in today's electronics industry. The need for low power


an in-depth survey of CAD methodologies and techniques for designing low power digital CMOS circuit abstraction. It reviews some of the techniques and tool ...

Keywords: CMOS circuits, adiabatic circuits, computer-aided design of VLSI, dynamic power dissipation and estimation, power management, power minimization and management, probabilistic analysis, system design

5 The CRITTER system: Automated critiquing of digital circuit designs

Van E. Kelly
June 1984

Proceedings of the 21st conference on Design automation

Full text available:  pdf(588.09 KB)


Additional Information: [full citation](#)

CRITTER is an exploratory prototype design aid, built using Artificial Intelligence ("Expert Systems" methodology), robustness, and circuit sensitivity to changes in device parameters. Its non-procedural representation of ordinary machine ...

6 Illustrative risks to the public in the use of computer systems and related technology

Peter G. Neumann

January 1996 **ACM SIGSOFT Software Engineering Notes**, Volume 21 Issue 1

Full text available:  pdf(2.54 MB)



Additional Information: [full citation](#)

7 The FINITE STRING Newsletter: Abstracts of current literature

Computational Linguistics Staff

January 1987 **Computational Linguistics**, Volume 13 Issue 1-2

Full text available:

 pdf(6.15 MB)  [Publisher Site](#)

Additional Information: [full citation](#)

8 Columns: Risks to the public in computers and related systems

Peter G. Neumann

January 2001 **ACM SIGSOFT Software Engineering Notes**, Volume 26 Issue 1

Full text available:  pdf(3.24 MB)

Additional Information: [full citation](#)

9 A reconfigurable dual output low power digital PWM power converter

Abram Dancy, Anantha Chandrakasan

August 1998

Proceedings of the 1998 international symposium on Low power electronics and design

Full text available:  pdf(670.26 KB)

Additional Information: [full citation](#)


Most work to date on power reduction has focused at the component level, not at the system level of resources, an environmental workload specification, and a power management policy, which seek to minimize power dissipation. Accompanying this work is a ...

10 Systems semantics: principles, applications, and implementation

Ray Boute

January 1988

ACM Transactions on Programming Languages and Systems (TOPLAS)

Full text available:  pdf(2.71 MB)

Additional Information: [full citation](#)

Systems semantics extends the denotational semantics of programming languages to a semantics of system functions, the same formal description may be used to denote different system properties, such as ...

provides guidance in language design, ...

11 Overview of the power minimization techniques employed in the IBM PowerPC 4xx embedded

Anthony Correale

April 1995 **Proceedings of the 1995 international symposium on Low power design**

Full text available:  [pdf\(44.29 KB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

12 Abstract state machines capture parallel algorithms

Andreas Blass, Yuri Gurevich

October 2003

ACM Transactions on Computational Logic (TOCL), Volume 4 Issue 4

Full text available:  [pdf\(610.28 KB\)](#)

Additional Information: [full](#)

We give an axiomatic description of parallel, synchronous algorithms. Our main result is that every

Keywords: ASM thesis, Parallel algorithm, abstract state machine, postulates for parallel computation

13 Development and application of NASA's first standard spacecraft computer

Charles E. Trevathan, Thomas D. Taylor, Raymond G. Hartenstein, Ann C. Merwarth, William N. Stevens
September 1984 **Communications of the ACM**, Volume 27 Issue 9

Full text available:  [pdf\(1.26 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [c](#)

To provide the autonomy needed by low, earth-orbiting satellites, NASA's first standard on-board


Keywords: PASS, avionics system

14 Object-oriented logical specification of time-critical systems

Angelo Morzenti, Pierluigi San Pietro

January 1994

ACM Transactions on Software Engineering and Methodology (TOSEM)

Full text available:  [pdf\(3.05 MB\)](#)

Additional Information: [f](#)

We define TRIO+, an object-oriented logical language for modular system specification. TRIO+ is a language that provides an effective support to a variety of validation activities, like specification testing, simulation

Keywords: first-order logic, formal specifications, model-theoretic semantics, object-oriented modeling

15 New models and architectures: Spatial computation

Mihai Budiu, Girish Venkataramani, Tiberiu Chelcea, Seth Copen Goldstein

October 2004

Proceedings of the 11th international conference on Architectural synthesis

Full text available:  [pdf\(573.00 KB\)](#)

Additional Information: [f](#)

This paper describes a computer architecture, *Spatial Computation (SC)*, which is based on the traditional distributed, with no centralized control. SC circuits are optimized for *wires* at the expense of computation assumption that computation is cheaper than communication ...

Keywords: application-specific hardware, dataflow machine, low-power, spatial computation

16 Formal verification in hardware design: a survey

Christoph Kern, Mark R. Greenstreet

April 1999

ACM Transactions on Design Automation of Electronic Systems (TO

Full text available:  [pdf\(411.53 KB\)](#)

Additional Information: [f](#)

In recent years, formal methods have emerged as an alternative approach to ensuring the quality and testing. There are two main aspects to the application of formal methods in a design process: about the relationship between a spec ...


Keywords: case studies, formal methods, formal verification, hardware verification, language cor

17 A true single-phase 8-bit adiabatic multiplier

Suhwan Kim, Conrad H. Ziesler, Marios C. Papaefthymiou

June 2001

Proceedings of the 38th conference on Design automation

Full text available:  [pdf\(647.64 KB\)](#)

Additional Information: [f](#)

This paper presents the design and evaluation of an 8-bit adiabatic multiplier. Both the multiplier and adiabatic circuitry via a sinusoidal power-clock waveform that is generated on-chip. In HSPICE simulation, the dissipation of the multiplier core is ...



Keywords: CMOS, SCAL, SCAL-D, VLSI, adiabatic logic, clock generator, dynamic logic, low energy

18 Effects of variable initiative on linguistic behavior in human-computer spoken natural language

Ronnie W. Smith, Steven A. Gordon

March 1997

Computational Linguistics, Volume 23 Issue 1

Full text available:  [pdf\(1.99 MB\)](#)  [Publisher Site](#)

Additional Information: [f](#)

This paper presents an analysis of the dialogue structure of actual human-computer interactions. It is organized around the paradigm of the Missing Axiom Theory for language use. Results about utterance frequency of linguistic control shifts, and frequency of ...

19 A static power model for architects

J. Adam Butts, Gurindar S. Sohi

December 2000

Proceedings of the 33rd annual ACM/IEEE international symposium on Micro



Full text available:  [pdf\(136.88 KB\)](#)  [ps\(431.76 KB\)](#)  Additional Information: [full citation](#), [references](#), [citations](#), [index](#)
[Publisher Site](#)

20 Razor: A Low-Power Pipeline Based on Circuit-Level Timing Speculation

Dan Ernst, Nam Sung Kim, Shidhartha Das, Sanjay Pant, Rajeev Rao, Toan Pham, Conrad Ziesler, D

December 2003

Proceedings of the 36th Annual IEEE/ACM International Symposium

Full text available:  [pdf\(568.17 KB\)](#)  [Publisher Site](#)

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With increasing clock frequencies and silicon integration, power aware computing has become a critical power-aware computing is dynamic voltage scaling (DVS). In order to obtain the maximum power savings, The critical voltage is chosen such that under a worst-case ...